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LSI Logic Corporation  
1621 Barber Lane  
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Milpitas, CA 95035

Date July 6, 2006

Number of pages including cover sheet 4

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TC 2123

Fax No. (571) 273-8300

Phone No.

CC:

From: Mark Salvatore

Intellectual Property Paralegal

Telephone (408) 433-7472  
No.

Fax No. (408) 433-7460

## REMARKS:

☐ Urgent ☐ For your review ☒ Reply ASAP ☐ Please comment

Application Number: 10/736,386  
Filing date: December 15, 2003  
First named inventor: Whitefield, Bruce  
Attorney docket number: 03-1345

Transmitted herewith for filing via facsimile:

- Transmittal Form, PTO/SB/21
- Information Disclosure Statement (letter)
- Information Disclosure Statement by Applicant PTO/SB/O&A

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PTO/SB/21 (09-04)

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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	10/735,386	
	Filing Date	December 15, 2003	
	First Named Inventor	Whitefield, Bruce	
	Art Unit	2123	
	Examiner Name	N/A	
Total Number of Pages in This Submission	3	Attorney Docket Number	03-1345

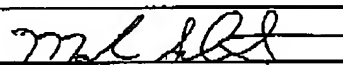
ENCLOSURES (Check all that apply)		
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## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	LSI Logic Corporation		
Signature			
Printed name	Timothy R. Croll		
Date	5 July 06	Reg. No.	36,771

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Signature			
Typed or printed name	Mark Salvatore	Date	7-6-06

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Bruce Whitefield et. al.

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Serial No.: 10/736,386

Group Art Unit: 2123

JUL 06 2006

Filed: 15-December-2003

Examiner:

For: Method For Calculating High-Resolution  
Wafer Param

Atty Docket: 03-1345

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Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

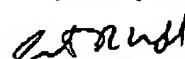
If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milpitas, CA 95035  
408-433-7475

Date:

5 JULY 06

Respectfully submitted,



Timothy Croll

Reg. No. 36,771

PTQ/SB/08A (07-05)

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Substitute for form 1449/PTO

(Use as many sheets as necessary)

**Complete if Known**

Application Number	10/736,386
Filing Date	December 15, 2003
First Named Inventor	Whitefield, Bruce
Art Unit	2123
Examiner Name	N/A
Attorney Docket Number	03-1345

Sheet	1	of	1
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## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

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Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinda Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language translation is attached.

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